
EE 491 Weekly Report (Group 22) October 27 -- November 2 Week 9 (10/27/14-11/2/14)

Advisors: *Timothy A Bigelow*

Client:

Members (roles): Aaron Tainter (Programming Lead), Haoyu Wang (Presentations & Documents), Jingyu Xie (Scheduler & Documents)

Project Title:

Programming ultrasound functional brain imaging system

Weekly Summary

Since this week our advisor is out of town, we made our own meeting with group members . During the meeting, we discussed rescheduling and individual requirements for the following weeks in order to be successful with our project. Specific tasks were assigned to our 3 group members in order to prepare for when we finally receive the hardware.

We also worked individually on other components of the project. These include: PCB test board development which is going to be finished at the end of this semester, configuration of NI 5752 cards, initialization of I/O on the labview system, and reading the NI hardware documentation. Additionally, we finished the project website.

Meeting notes:

10/28 **Group Meeting with Group members**

Duration: 3 hours

Members Present: Aaron Tainter, Haoyu Wang, Jingyu Xie

Purpose and Goals:

Our weekly meeting with Group members and reallocate tasks to 3 of us to make sure we can make tasks complete on time.

Achievements:

We talked about issues that need to be solved in the next few weeks while we are still waiting on NI hardware and we have already started to design the adaptor which is the most important part of the hardware. Conflicts with software on the NI system were finally solved and Aaron was tasked with communication with NI technical support team. Jingyu was told to finish developing the test board and the initial configuration of NI cards, while Haoyu was told to go

through NI hardware documentation so that we have information about the parts when we receive them and know how to deal with the operation of the hardware.

Pending issues

1. Design and finish PCB board.
2. Configure NI cards with labVIEW program
3. Develop a simple I/O program to test them.

Plans for next week

1. Configure NI system and Develop I/O program. (Aaron)
2. Continue working on developing PCB. (Jingyu Xie)
3. Go through NI hardware documentation. (Haoyu Wang)

Individual Contributions(this week)

1. Aaron Tainter(website work - 1.5 hr,
configuring NI cards/dev work 4hrs)
2. Haoyu Wang(Research work on NI
documentation 2hr, meeting with group 3hr)
3. Jingyu Xie(Developing PCB board - 5hr,
meeting with group 3hr)