EE 491 Weekly Report (Group 22) October 6 -- October 12 Week 6 (10/06/14-10/12/14)

Advisors: *Timothy A Bigelow*   **Client:**

Members (roles): Aaron Tainter (Programming Lead), Weikun Han (Documents), Haoyu Wang (Presentations), Jingyu Xie (Scheduler)

Project Title:

 Programming ultrasound functional brain imaging system

Weekly Summary

This week our group held 2 meetings. In the first meeting, we talked to professor Bigelow about project design and plans for a PCB test board. In the second, we talked to Lee in the tech shop and began learning software used for PCB development. We also put some work into the project website.

Meeting notes:

10/06   Group Meeting with Advisor

Duration: *1 hours*   **Members Present: Aaron Tainter, Weikun Han, Haoyu Wang**

Purpose and Goals:
Our weekly meeting with Professor Bigelow

Achievements:
We talked about PCB development and the status of the ultrasound hardware. Clarifications for more design requirements were also discussed.

10/09 Group Meeting for PCB development

Duration: *3 hours*   **Members Present: Weikun Han, Haoyu Wang, Jinyu Xie**

 Purpose and Goals:
 In this meeting group members worked on learning the software used for PCB development.

 Achievements:

To obtain the PCB test board design outline which is design for the hardware devices. Understand the port of the hardware design provided and design only one port on the PCB.

Pending issues

1. Develop PCB board
2. Finish website
3. Work on labVIEW concepts and contact NI about test board alternatives

Plans for next week

1. Continue website development (Aaron)
2. Contact NI support. (Aaron)
3. Begin working on developing PCB. (Weikun Han, Haoyu Wang, Jingyu Xie)
4. Develop labVIEW test programs. (Whole team)

Individual Contributions(this week)

1. Aaron Tainter(meeting with Bigelow - 1hr, website work - 2.5 hrs, labVIEW study - 2hrs, troubleshooting NI hardware 2hrs)
2. Weikun Han (meeting with Bigelow - 1 hour, design PCB test board - 1.5 hours, LabVIEW study - 6 hours)
3. Jinyu Xie (individual meeting with professor Bigelow - 1 hour, design PCB test board - 0.5 hours, LabVIEW study - 2 hours, hardware devices port information check - 0.5 hours)
4. Haoyu Wang (meeting with Bigelow - 1 hour, design PCB test board - 0.5 hours, LabVIEW study - 1.5 hours, test board information search - 0.5 hours, communication with group member and set up meeting time - 0.5 hours)